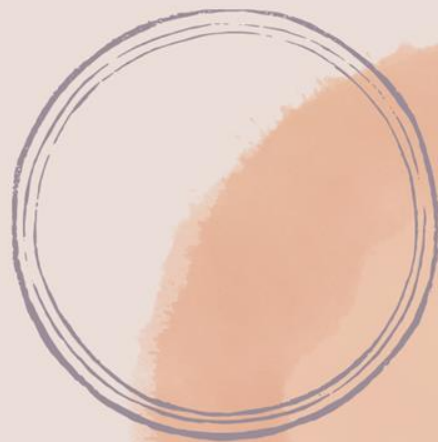


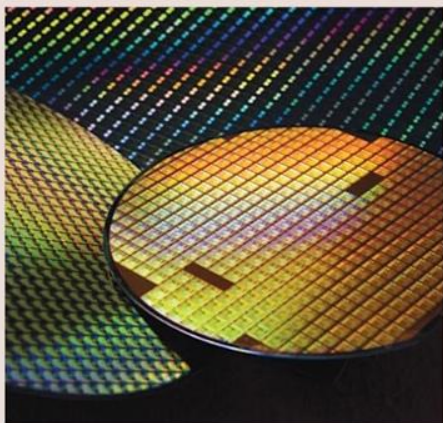
# 積體電路

# 與數位邏輯



演講者: 陳詠玟、黃嘉萱、程素言

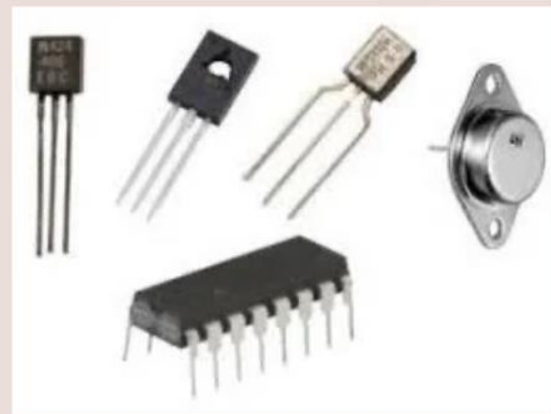
晶圓

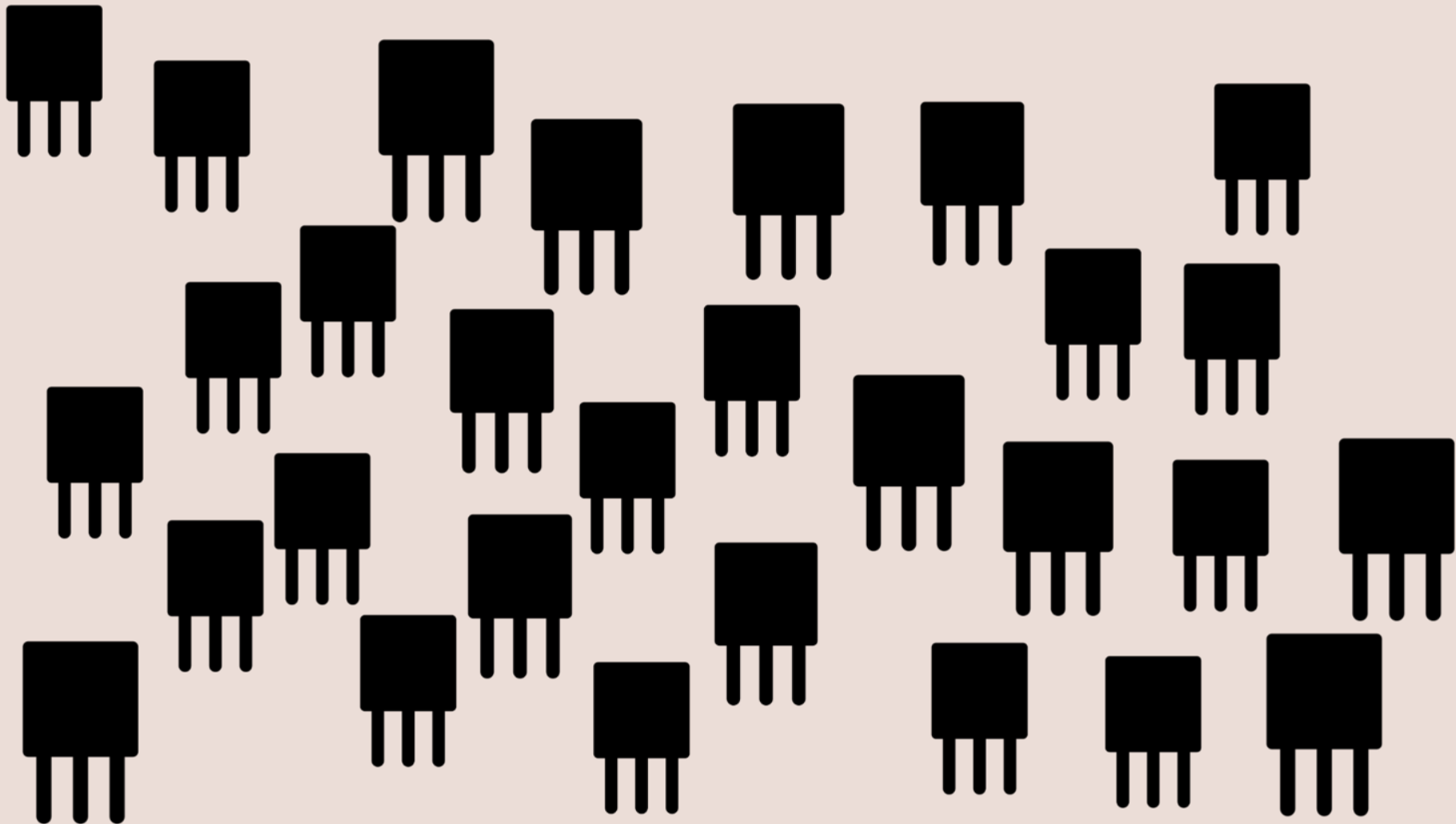


積體電路（晶片）

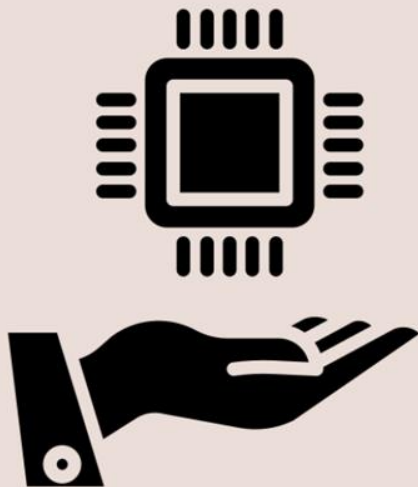


電晶體









二進位





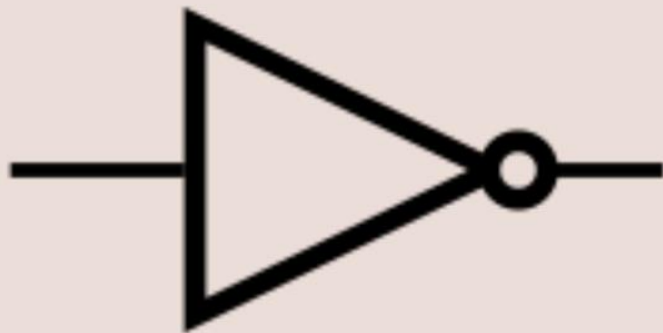
- 逢二進一 (加法)
- 借一當二 (減法)
- 先乘後加 (乘法)
- 先除後減 (除法)



AND



NOT



OR



XOR



謝謝大家